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Notice of Allowability	Application No.	Applicant(s)
	09/758,290	YAMAMOTO ET AL.
	Examiner	Art Unit
	Kimbinh T. Nguyen	2671

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS**. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. This communication is responsive to amendment filed 09/02/04.
2. The allowed claim(s) is/are 3-6,10-13 and 17-20.
3. The drawings filed on 02 September 2004 are accepted by the Examiner.
4. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All
 - b) Some*
 - c) None
 of the:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
6. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) hereto or 2) to Paper No./Mail Date _____.
 - (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. Notice of References Cited (PTO-892)
2. Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date _____.
4. Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. Notice of Informal Patent Application (PTO-152)
6. Interview Summary (PTO-413),
Paper No./Mail Date _____.
7. Examiner's Amendment/Comment
8. Examiner's Statement of Reasons for Allowance
9. Other _____.

DETAILED ACTION

1. This action is responsive to amendment filed 09/02/2004.
2. Claims 3-6, 10-13 and 17-20 are pending in the application.
3. The amendment in the specification filed 09/02/2004 has been entered.

Drawings

4. The drawings were received on 09/02/2004. These drawings are Figure 1, Figure 6, Figure 7 and Figure 8 are accepted by the examiner.

Reasons for Allowance

5. Claims 3-6, 10-13 and 17-20 are allowed.

The following is an examiner's statement of reasons for allowance:

Claim 3, the prior art does not teach a random access memory (RAM) incorporated display driver for displaying displayed data stored in the incorporated on a display screen, comprising: an access control circuit configured to read out the display data from said RAMs send the read out display data to said latch shift register, and if the display screen is intended to be scrolled in a horizontal directions write back the display data shifted by said latch shift register into an original region in said and if said display screen is intended to be scrolled in a vertical direction, write back the display data held by said latch shift register into a region in said RAM moved by the amount of the scroll from the original region of said RAM, and supply the written back display data in said RAM to said display screen according to a screen control signal supplied by a CPU configured outside of the display driver; and a selecting circuit configured to select

a region in a horizontal direction capable of being scrolled in the display screen, wherein said access control circuit supplies display data in a region selected by said selecting circuit to said latch shift register.

Claim 4, the prior art does not teach a random access memory (RAM) incorporated display driver for displaying displayed data stored in the incorporated on a display screen, comprising: an access control circuit configured to read out the display data from said RAMs send the read out display data to said latch shift register, and if the display screen is intended to be scrolled in a horizontal directions write back the display data shifted by said latch shift register into an original region in said and if said display screen is intended to be scrolled in a vertical direction, write back the display data held by said latch shift register into a region in said RAM moved by the amount of the scroll from the original region of said RAM, and supply the written back display data in said RAM to said display screen according to a screen control signal supplied by a CPU configured outside of the display driver; and a selecting circuit configured to select a region in a vertical direction capable of being scrolled in the display screen, wherein said access control circuit supplies display data in a region selected by said selecting circuit to said latch shift register.

Claim 10 discloses an image apparatus for displaying data in an incorporated RAM, the claim comprising the claimed elements same as claim 3 and is allowed under the same reasons set forth in claim 3.

Claim 11 discloses an image apparatus for displaying data in an incorporated RAM, the claim comprising the claimed elements same as claim 4 and is allowed under the same reasons set forth in claim 4.

Claim 17, the prior art does not teach a random access memory (RAM) incorporated display driver for displaying displayed data stored in the incorporated on a display screen, comprising: an access control circuit configured to read out the display data from said RAMs send the read out display data to said latch shift register, and if the display screen is intended to be scrolled in a horizontal directions write back the display data shifted by said latch shift register into an original region in said and if said display screen is intended to be scrolled in a vertical direction, write back the display data held by said latch shift register into a region in said RAM moved by the amount of the scroll from the original region of said RAM, and supply the written back display data in said RAM to said display screen according to a screen control signal supplied by a CPU configured outside of the display driver; a selecting unit configured to select a region in a lateral direction capable of being scrolled in the display screen, wherein said access control unit supplies display data in a region selected by said selecting unit to said latch shift unit.

Claim 18, the prior art does not teach a random access memory (RAM) incorporated display driver for displaying displayed data stored in the incorporated on a display screen, comprising: an access control circuit configured to read out the display data from said RAMs send the read out display data to said latch shift register, and if the display screen is intended to be scrolled in a horizontal directions write back the

display data shifted by said latch shift register into an original region in said and if said display screen is intended to be scrolled in a vertical direction, write back the display data held by said latch shift register into a region in said RAM moved by the amount of the scroll from the original region of said RAM, and supply the written back display data in said RAM to said display screen according to a screen control signal supplied by a CPU configured outside of the display driver; a selecting unit configured to select a region in a longitudinally direction capable of being scrolled in the display screen, wherein said access control unit supplies display data in a region selected by said selecting unit to said latch shift unit.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kimbinh T. Nguyen whose telephone number is (703) 305-9683. The examiner can normally be reached on Monday to Thursday from 7:00 AM to 4:30 PM. The examiner can also be reached on alternate Friday from 7:00 AM to 3:30 PM.

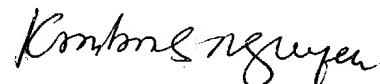
If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mark Zimmerman, can be reached at (703) 305-9798. The fax phone

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number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

September 29, 2004



Kimbinh Nguyen

Patent Examiner AU 2671